

Remarks/Arguments

Claims 1 – 16 are pending, claims 1, 5, 9 and 12 – 16 are rejected under Double Patenting and claims 2 – 4, 6 – 8 and 10 – 11 are objected to as being dependent on a rejected base claim. Claims 1, 3, 9, 11, 13 and 15 have been amended and claims 2 and 10 have been cancelled.

DOUBLE PATENTING

Examiner has rejected claims 1, 5, 9 and 12 – 16 on the ground of non-statutory obviousness type double patenting, citing US 7,119,774. Applicant has amended independent claims 1, 9, 13 and 15 to render them patentably distinct from US 7,119,774. Amended claim 1 recites, inter alia, “a split low pass filter arrangement for independently low pass filtering rising transients and lengthening a fall time of falling transients in said low brightness signal to reduce adjacent pixel interdependence, wherein the split low pass filter arrangement comprises at least two low pass filters, at least one associated delay circuit, and a maximum selector circuit.” The added feature of “the split low pass filter arrangement comprises at least two low pass filters, at least one associated delay circuit, and a maximum selector circuit” is neither disclosed nor suggested in US 7,119,774. As such, it is clear that the present application is an improvement over US 7,119,774 and is patentably distinct from it. Rejection of claim 1 is thus traversed. Applicant respectfully requests withdrawal of the rejection of claim 1.

Amended claim 9 similarly recites “low pass filtering said low brightness signal according to a first filtering rate to generate a first filtered value; delay matching and low pass filtering said low brightness signal according to a second filtering rate to generate a second filtered value; selecting as a filtered output for use in said combining step the maximum of said first and second filtered values;”. The added steps of “low pass filtering said low brightness signal according to a first filtering

rate to generate a first filtered value; delay matching and low pass filtering said low brightness signal according to a second filtering rate to generate a second filtered value; (and) selecting as a filtered output for use in said combining step the maximum of said first and second filtered values” are neither disclosed nor suggested in US 7,119,774. As such, it is clear that the present application is an improvement over US 7,119,774 and is patentably distinct from it. Rejection of claim 9 is thus traversed. Applicant respectfully requests withdrawal of the rejection of claim 9.

Amended claim 13 similarly recites “a filter coupled to said low brightness output, wherein the filter comprises at least two low pass filters, at least one associated delay circuit, and a maximum selector circuit”. The added features of claim 13 are neither disclosed nor suggested in US 7,119,774. As such, it is clear that the present application is an improvement over US 7,119,774 and is patentably distinct from it. Rejection of claim 13 is thus traversed. Applicant respectfully requests withdrawal of the rejection of claim 13.

Amended claim 15 similarly recites “filtering said second video signal portion according to a first filtering rate to generate a first filtered value, and delay matching and filtering said second video signal portion according to a second filtering rate to generate a second filtered value, to adjust transition times of said pulses so as to reduce adjacent pixel interdependence”. The added features of claim 15 are neither disclosed nor suggested in US 7,119,774. As such, it is clear that the present claim 15 is patentably distinct from US 7,119,774. Rejection of claim 15 is thus traversed. Applicant respectfully requests withdrawal of the rejection of claim 15.

Claims 3 – 8, 11, 12, 14 and 16, being dependent on and adding advantageous features to allowable base claims or claims dependent on allowable base claims are themselves patentable as the allowable base claims. Applicant respectfully requests rejection of claims 3 – 8, 11, 12, 14 and 16 be withdrawn.

Having fully addressed the Examiner's objections and rejections it is believed that, in view of the preceding amendments and remarks, this application stands in condition for allowance. Accordingly then, reconsideration and allowance are respectfully solicited. If, however, the Examiner is of the opinion that such action cannot be taken, the Examiner is invited to contact the applicant's attorney at (317) 587-4029, so that a mutually convenient date and time for a telephonic interview may be scheduled.

No additional fee is believed due. However, if an additional fee is due, please charge the additional fee to Deposit Account 07-0832.

Respectfully submitted,




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January 25, 2008

CERTIFICATE OF MAILING

I hereby certify that this amendment is being deposited with the United States Postal Service as First Class Mail, postage prepaid, in an envelope addressed to Mail Stop AMENDMENT, Commissioner for Patents, Alexandria, Virginia 22313-1450 on:

1/25/08
Date


William A. Lagoni